

A Kripke Logical relation between ML & Assembly

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Observational equivalence between two languages

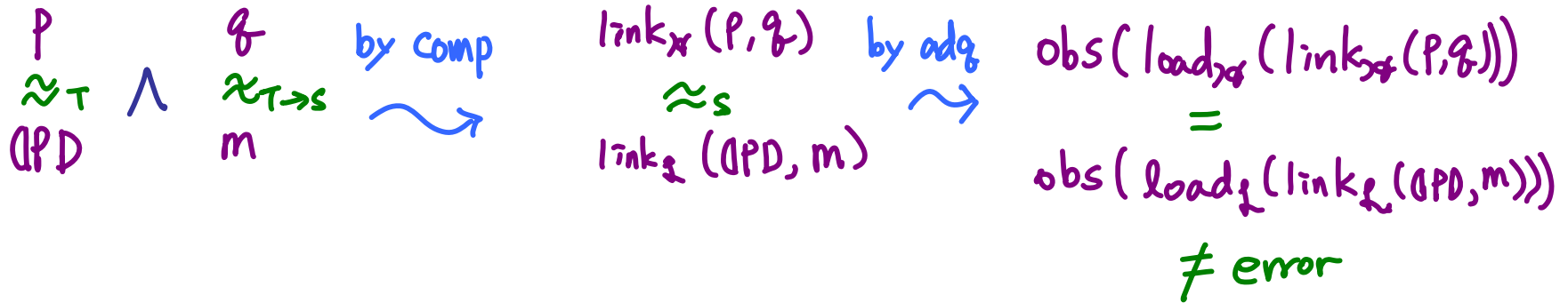
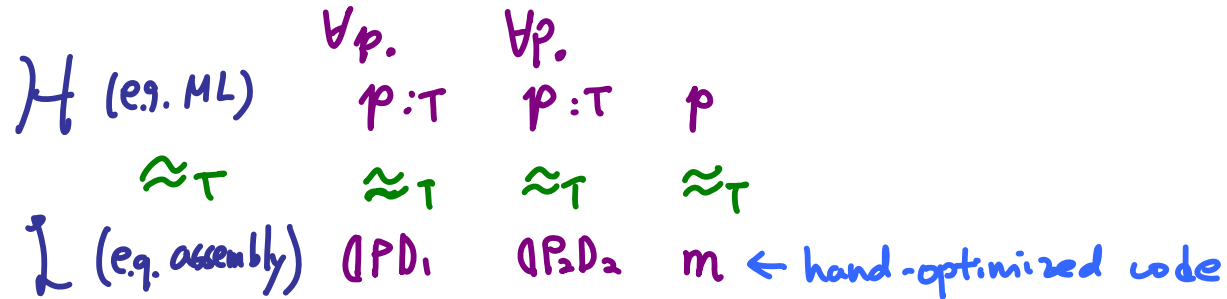
$$\begin{array}{ccc} \mathcal{L}_1 & & \mathcal{L}_2 \\ p_1 & \approx & p_2 \end{array}$$

Q: A good notion of program equivalence between \mathcal{L}_1 and \mathcal{L}_2 ?

Requirements

- ① type-indexed relations e.g.) $\lambda x.x \approx_{\text{int} \rightarrow \text{int}} \lambda x.x+0$
- ② adequate : $p_1 \approx_T p_2 \Rightarrow \text{obs}(\text{load}_{\mathcal{L}_1}(p_1)) = \text{obs}(\text{load}_{\mathcal{L}_2}(p_2)) \neq \text{error}$
- ③ compositional : $p_1 \approx_T p_2 \wedge k_1 \approx_{T \rightarrow S} k_2 \Rightarrow \text{link}_{\mathcal{L}_1}(k_1, p_1) \approx_S \text{link}_{\mathcal{L}_2}(k_2, p_2)$
- ④ extensional (informal concepts) : sufficiently populated

Application: Compositional Compiler Correctness



Overview

Language-generic logical relation

→ Logical relation between Assembly and $\lambda^{\forall, \exists, \mu, \text{ref}}$

- self modifying code ←

- garbage collector
(Mark-Sweep, Copying)



Adq & Comp \leadsto Compos. Compiler Correct.

Based on

- Logical relation between SECD and $\lambda^{\text{fix}, \forall, \exists}$

(ICFP'09, MSR techrep: Benton & Hur) \leadsto Basic idea of
Compositional Compiler
Correctness

- Logical relation on $\lambda^{\forall, \exists, \mu, \text{ref}}$

(ICFP'10: Dreyer, Neis & Birkedal) \leadsto possible worlds model
as STS with priv. vs pub.

- step-indexing (Appel & McAllester)
- biorthogonality (Krivine; Pitts & Stark)

Language : High

HIGH – Syntax & Semantics

$\tau ::= \alpha \mid b \mid \tau_1 \times \tau_2 \mid \tau_1 \rightarrow \tau_2 \mid \forall \alpha. \tau \mid \exists \alpha. \tau \mid \mu \alpha. \tau \mid \text{ref } \tau$

$e ::= x \mid \ell \mid \langle e_1, e_2 \rangle \mid e.1 \mid e.2 \mid \lambda x:\tau. e \mid e_1 e_2 \mid \Lambda \alpha. e \mid e \tau \mid$
 $\text{pack } \langle \tau_1, e \rangle \text{ as } \tau_2 \mid \text{unpack } e_1 \text{ as } \langle \alpha, x \rangle \text{ in } e_2 \mid$
 $\text{roll}_\tau e \mid \text{unroll } e \mid \text{ref } e \mid e_1 := e_2 \mid !e \mid e_1 == e_2 \mid \dots$

$v ::= x \mid \ell \mid \langle v_1, v_2 \rangle \mid \lambda x:\tau. e \mid \Lambda \alpha. e \mid \text{pack } \langle \tau_1, v \rangle \text{ as } \tau_2 \mid \text{roll}_\tau v \mid \dots$

$K ::= \bullet \mid \langle K, e_2 \rangle \mid \langle v_1, K \rangle \mid K.1 \mid K.2 \mid K e_2 \mid v_1 K \mid K \tau \mid \text{roll}_\tau K \mid$
 $\text{unroll } K \mid \text{pack } \langle \tau_1, K \rangle \text{ as } \tau_2 \mid \text{unpack } K \text{ as } \langle \alpha, x \rangle \text{ in } e_2 \mid$
 $\text{ref } K \mid K := e_2 \mid v_1 := K \mid !K \mid K == e_2 \mid v_1 == K \mid \dots$

$\Sigma ::= \cdot \mid \Sigma, \ell:\tau \text{ with } \text{ftv}(\tau) = \emptyset \quad \Delta ::= \cdot \mid \Delta, \alpha \quad \Gamma ::= \cdot \mid \Gamma, x:\tau$

Static semantics : $\Sigma; \Delta; \Gamma \vdash e : \tau$

$\text{HCVal} \stackrel{\text{def}}{=} \{ v \mid \text{ftv}(v) = \emptyset \wedge \text{fv}(v) = \emptyset \}$

$\text{HHeap} \stackrel{\text{def}}{=} \{ h \in \text{HLoc} \rightarrow_{\text{fin}} \text{HCVal} \}$

Dynamic semantics : $(h, e) \hookrightarrow (h', e')$

Language : Low

LOW – Syntax

$PConf \stackrel{\text{def}}{=} \{ (\Phi, pc) \in PMem \times PAddr \}$

$PMem \stackrel{\text{def}}{=} \{ \Phi = (\text{code}, \text{reg}, \text{stk}, \text{hp}) \in PCode \times \text{RegFiles} \times \text{Stack} \times \text{Heap} \}$

$PCode \stackrel{\text{def}}{=} PAddr \rightarrow \text{Instruction}$ $PRegFile \stackrel{\text{def}}{=} \text{Register} \rightarrow PWord$

$PStack \stackrel{\text{def}}{=} PAddr \rightarrow PWord$ $PHeap \stackrel{\text{def}}{=} PAddr \rightarrow PWord$

$PAddr \stackrel{\text{def}}{=} \{ a \in \mathbb{N} \}$ $PWord \stackrel{\text{def}}{=} \{ w \in \{0, 1\} \times \mathbb{N} \}$

$r \in \text{Register} ::= \text{sp} \mid \text{sv}_0 \mid \dots \mid \text{sv}_4 \mid \text{wk}_0 \mid \dots \mid \text{wk}_5$

$lv \in PLvalue ::= \lfloor r \rfloor \mid \langle a \rangle_s \mid \langle r - o \rangle_s \mid \langle a \rangle_h \mid \langle r + o \rangle_h$

$rv \in PRvalue ::= lv \mid w$

$\iota \in \text{Instruction} ::= \text{fail} \mid \text{halt} \mid \text{jmp } rv \mid \text{jnz } rv \ rv \mid \text{jneq } rv \ rv \ rv \mid$
 $\text{jptr } rv \ rv \mid \text{setptr } lv \mid \text{move } lv \ rv \mid \text{plus } lv \ rv \ rv \mid$
 $\text{minus } lv \ rv \ rv \mid \text{isr } lv \ rv \mid \text{isw } rv \ rv$

Awkward example

let $x = \text{ref } 0$ in $\lambda f : \text{unit} \rightarrow \text{unit}. (x := 1 ; f \langle \rangle ; !x)$
 \approx
 $\lambda f : \text{unit} \rightarrow \text{unit}. (f \langle \rangle ; 1)$

$p \stackrel{\text{def}}{=} \lambda \text{ alloc, bg. } [$

bg	move	[wk4]	<u>bg + 3</u>
	move	[wk5]	<u>1</u>
bg + 3	jmp	<u>alloc</u>	
	move	$\langle \text{wk5} + 0 \rangle_h$	<u>bg + 5</u>
	jmp	[wk0]	

} create a closure
and return it

Motivating example

bg + 5	move	[wk3]	bg + 10 <u>jmp <u>bg + 12</u></u>
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bg + 6	isr	[wk4]	[wk3]
	minus	[wk4]	[wk4] <u>666</u>
	isw	[wk3]	[wk4]
	plus	[wk3]	[wk3] <u>1</u>
bg + 10	D(E(jneq	bg + 6	[wk3] <u>bg + 21</u>) + 666)
bg + 11	D(E(isw	bg + 5	E(jmp <u>bg + 12</u>)) + 666)
bg + 12	D(E(move	$\langle \text{wk1} + 0 \rangle_h$	<u>bg + 13</u>) + 666)

} decoding

bg + 13	D(E(plus	[sp]	[sp] <u>1</u>) + 666)
	D(E(move	$\langle \text{sp} - 1 \rangle_s$	[wk0]) + 666)
	D(E(move	[wk1]	[wk2]) + 666)
	D(E(move	[wk0]	<u>bg + 18</u>) + 666)
	D(E(jmp	$\langle \text{wk1} + 0 \rangle_h$) + 666)
bg + 18	D(E(move	[wk5]	<u>1</u>) + 666)
	D(E(minus	[sp]	[sp] <u>1</u>) + 666)
bg + 20	D(E(jmp	$\langle \text{sp} - 0 \rangle_s$) + 666)]

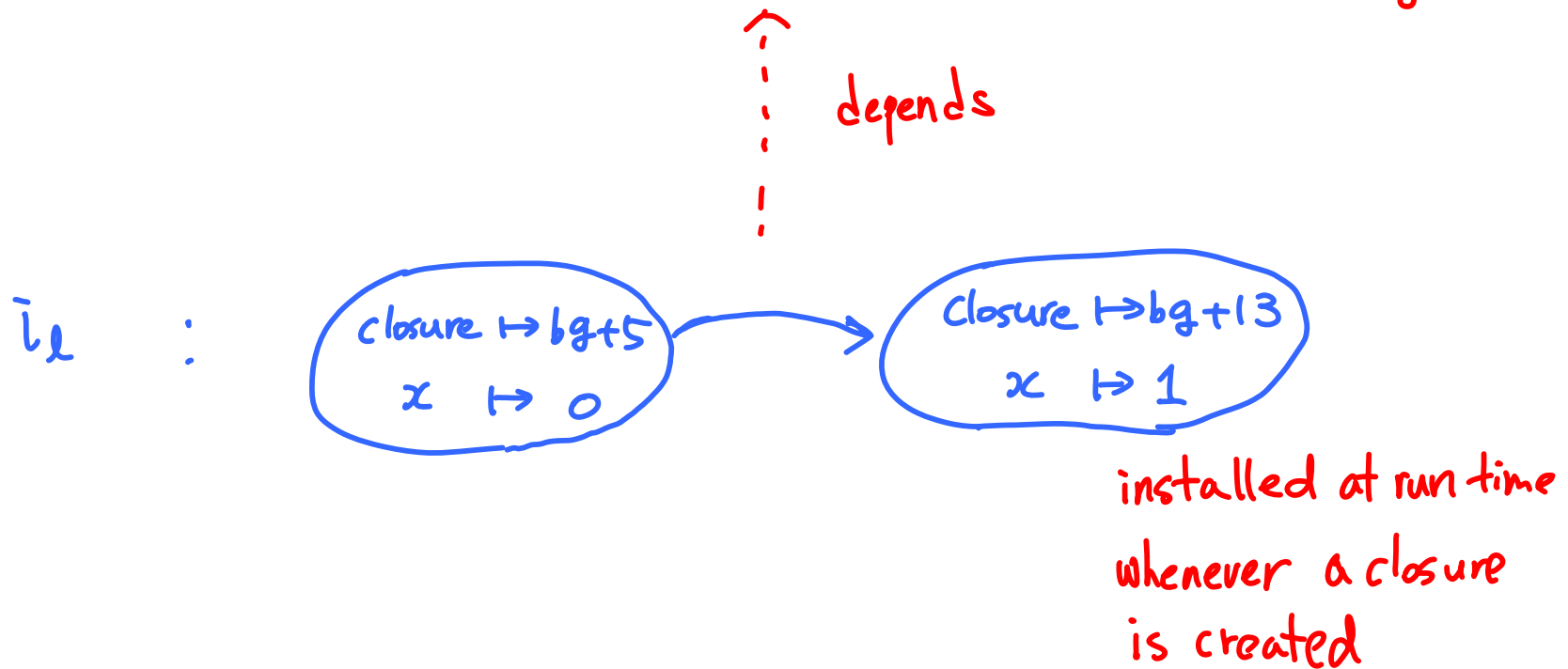
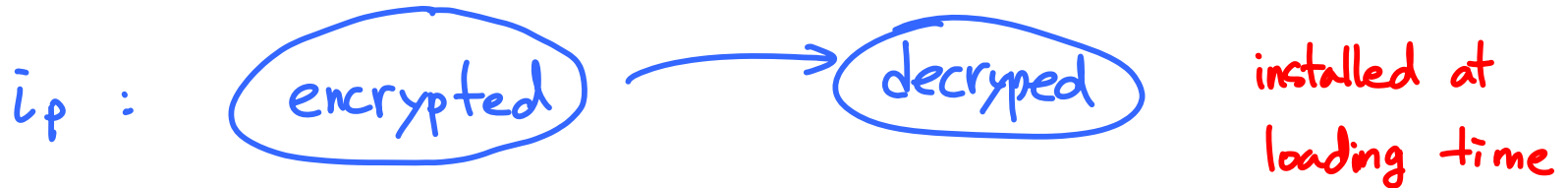
} $\lambda f. f\langle \rangle; 1$

Key ideas

- Island for program code
- Island for closures and local states
- Island for stack and registers
- Garbage Collection

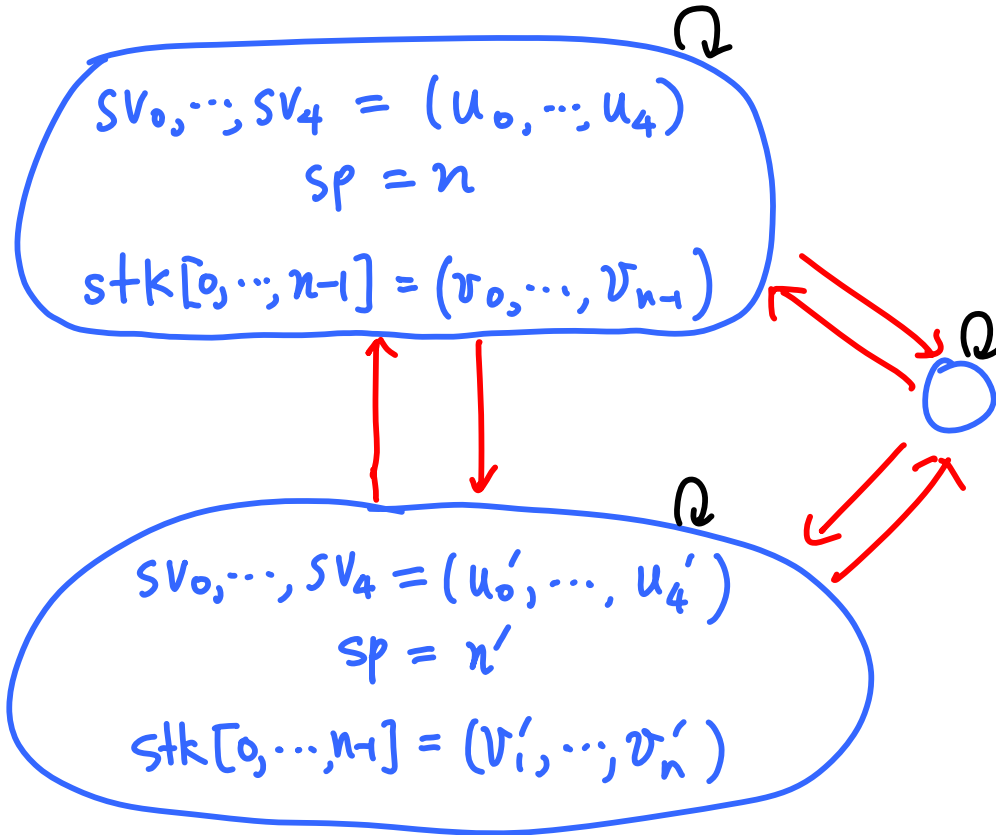
Islands for code, closures & local states

$e = \text{let } x := \text{ref } 0 \text{ in } (x := 1; f \langle \rangle; !x)$ $p = \dots$



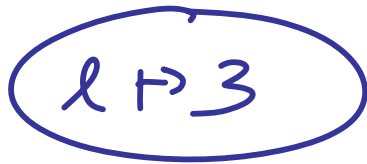
Island for Stack & Registers

— pub
— prv



Garbage Collection

Problem

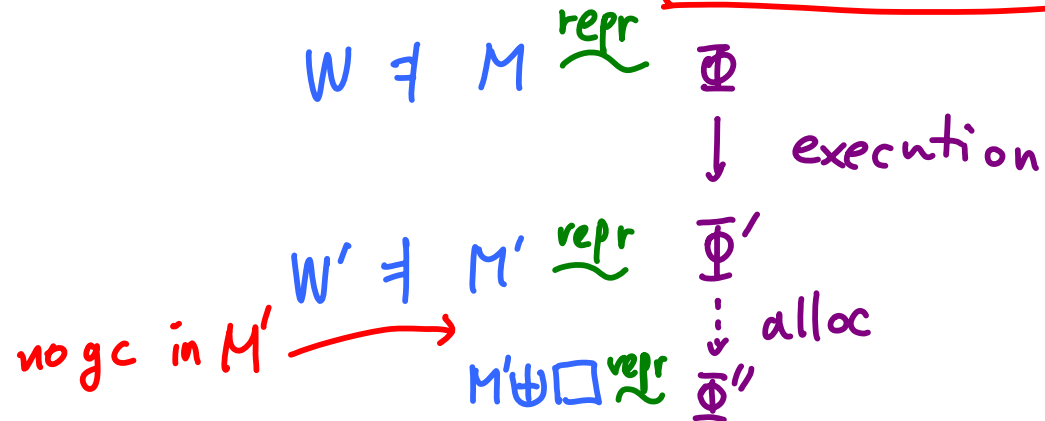


- if l is collected?
- if l is relocated?

Solution

Logical Memory!
(almost zero overhead)

Global invariant:
all reachable memories in M
are live in Φ



Language and World Specifications

$$\tau ::= \alpha \mid b \mid \tau_1 \times \tau_2 \mid \tau_1 \rightarrow \tau_2 \mid \forall \alpha. \tau \mid \exists \alpha. \tau \mid \mu \alpha. \tau \mid \text{ref } \tau$$

$$\text{CType} \stackrel{\text{def}}{=} \{ \tau \mid \text{ftv}(\tau) = \emptyset \}$$

$$\text{LangSpec} \stackrel{\text{def}}{=} \{$$

$$(\text{Val}, \text{Com}, \text{Cont}, \text{Mem}, \text{Conf},$$

$$\text{plugv}, \text{plugc}, \text{step}, \text{mdom}, \text{mdisj},$$

$$\text{oftype}, \text{base}_b, \text{pair}, \text{app}, \text{appty},$$

$$\text{pack}, \text{roll}, \text{ref}, \text{asgn}) \mid$$

$$\text{Val}, \text{Com}, \text{Cont}, \text{Mem}, \text{Conf} \in \text{Set} \wedge$$

$$\text{plugv} \in \text{Val} \times \text{Cont} \times \text{Mem} \rightarrow \mathbb{P}(\text{Conf}) \wedge$$

$$\text{plugc} \in \text{Com} \times \text{Cont} \times \text{Mem} \rightarrow \mathbb{P}(\text{Conf}) \wedge$$

$$\text{step} \in \text{Conf} \rightarrow \text{Conf} \uplus \{ \text{fail}, \text{halt} \} \wedge$$

$$\text{mdom} \in \text{Mem} \rightarrow \mathbb{P}(\text{Val}) \wedge$$

$$\text{mdisj} \in \text{Mem} \times \text{Mem} \rightarrow \mathbb{P}(\text{Mem}) \wedge$$

$$\text{oftype} \in \text{CType} \rightarrow \mathbb{P}(\text{Val} \times \text{Mem}) \wedge$$

$$\text{base}_b \in \llbracket b \rrbracket \rightarrow \mathbb{P}(\text{Val} \times \text{Mem}) \wedge$$

$$\text{pair} \in \text{Val} \times \text{Val} \rightarrow \mathbb{P}(\text{Val} \times \text{Mem}) \wedge$$

$$\text{app} \in \text{Val} \times \text{Val} \rightarrow \mathbb{P}(\text{Com}) \wedge$$

$$\text{appty} \in \text{Val} \times \text{CType} \rightarrow \mathbb{P}(\text{Com}) \wedge$$

$$\text{pack} \in \text{CType} \times \text{Val} \rightarrow \mathbb{P}(\text{Val} \times \text{Mem}) \wedge$$

$$\text{roll} \in \text{Val} \rightarrow \mathbb{P}(\text{Val} \times \text{Mem}) \wedge$$

$$\text{ref} \in \text{Val} \rightarrow \mathbb{P}(\text{Val} \times \text{Mem}) \wedge$$

$$\text{asgn} \in \text{Mem} \times \text{Val} \times \text{Val} \rightarrow \text{Mem} \wedge$$

$$\forall M_1, M_2. \forall M \in \text{mdisj}(M_1, M_2).$$

$$\text{mdom}(M) \supseteq \text{mdom}(M_1) \uplus \text{mdom}(M_2) \}$$

For $\mathcal{L}_1, \mathcal{L}_2 \in \text{LangSpec}$,

$$\text{WorldSpec} \stackrel{\text{def}}{=} \{$$

$$(\text{World}, \text{lev}, \mathcal{M}, \mathcal{B}, \mathcal{O}, \triangleright, \sqsupseteq, \sqsupseteq_{\text{pub}}) \mid$$

$$\text{World} \in \text{Set} \wedge$$

$$\text{lev} \in \text{World} \rightarrow \mathbb{N} \wedge$$

$$\mathcal{M} \in \text{World} \rightarrow \mathbb{P}(\mathcal{L}_1.\text{Mem} \times \mathcal{L}_2.\text{Mem}) \wedge$$

$$\mathcal{B} \in \text{World} \rightarrow \mathbb{P}(\mathcal{L}_1.\text{Val} \times \mathcal{L}_2.\text{Val}) \wedge$$

$$\mathcal{O} \in \text{World} \rightarrow \mathbb{P}(\mathcal{L}_1.\text{Conf} \times \mathcal{L}_2.\text{Conf}) \wedge$$

$$\triangleright \in \text{World} \rightarrow \text{World} \wedge$$

$$\sqsupseteq \in \mathbb{P}(\text{World} \times \text{World}) \wedge$$

$$\sqsupseteq_{\text{pub}} \in \mathbb{P}(\text{World} \times \text{World}) \wedge$$

$$\sqsupseteq, \sqsupseteq_{\text{pub}} \text{ are preorders} \wedge \sqsupseteq_{\text{pub}} \subseteq \sqsupseteq \wedge$$

$$\forall W' \sqsupseteq W. \triangleright W' \sqsupseteq \triangleright W \wedge$$

$$\forall W' \sqsupseteq_{\text{pub}} W. \triangleright W' \sqsupseteq_{\text{pub}} \triangleright W \wedge$$

$$\forall W. \triangleright W \sqsupseteq_{\text{pub}} W \wedge$$

$$\forall W' \sqsupseteq W. \text{lev}(W') \leq \text{lev}(W) \wedge$$

$$\forall W. \text{lev}(W) > 0 \implies \text{lev}(\triangleright W) = \text{lev}(W) - 1 \}$$

Logical relation

$$\begin{aligned}
 \mathcal{V}[\alpha]\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(\alpha, \rho) \mid (W, v_1, v_2) \in \square\rho(\alpha).R \} \\
 \mathcal{V}[b]\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(b, \rho) \mid \exists x \in [b] . \\
 &\quad (W, v_1, v_2) \in \square(\mathcal{L}_1.\text{base}_b(x), \mathcal{L}_2.\text{base}_b(x)) \} \\
 \mathcal{V}[\tau \times \tau']\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(\tau \times \tau', \rho) \mid \\
 &\quad \exists(u_1, u_2) \in \triangleright\mathcal{V}[\tau]\rho(W). \exists(u'_1, u'_2) \in \triangleright\mathcal{V}[\tau']\rho(W). \\
 &\quad (W, v_1, v_2) \in \square(\mathcal{L}_1.\text{pair}(u_1, u'_1), \mathcal{L}_2.\text{pair}(u_2, u'_2)) \} \\
 \mathcal{V}[\tau' \rightarrow \tau]\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(\tau' \rightarrow \tau, \rho) \mid \forall W' \sqsupseteq W. \forall(u_1, u_2) \in \mathcal{V}[\tau']\rho(W'). \\
 &\quad \forall e_1 \in \mathcal{L}_1.\text{app}(v_1, u_1). \forall e_2 \in \mathcal{L}_2.\text{app}(v_2, u_2). (W', e_1, e_2) \in \mathcal{E}[\tau]\rho \} \\
 \mathcal{V}[\forall\alpha.\tau]\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(\forall\alpha.\tau, \rho) \mid \forall W' \sqsupseteq W. \forall(\tau_1, \tau_2, R) \in \text{TyValRel}. \\
 &\quad \forall e_1 \in \mathcal{L}_1.\text{appty}(v_1, \tau_1). \forall e_2 \in \mathcal{L}_2.\text{appty}(v_2, \tau_2). \\
 &\quad (W', e_1, e_2) \in \mathcal{E}[\tau]\rho[\alpha \mapsto (\tau_1, \tau_2, R)] \} \\
 \mathcal{V}[\exists\alpha.\tau]\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(\exists\alpha.\tau, \rho) \mid \\
 &\quad \exists(\tau_1, \tau_2, R) \in \text{TyValRel}. \exists(u_1, u_2) \in \mathcal{V}[\tau]\rho[\alpha \mapsto (\tau_1, \tau_2, R)](W). \\
 &\quad (W, v_1, v_2) \in \square(\mathcal{L}_1.\text{pack}(\tau_1, u_1), \mathcal{L}_2.\text{pack}(\tau_2, u_2)) \} \\
 \mathcal{V}[\text{ref } \tau]\rho &\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \in \text{oftype}(\text{ref } \tau, \rho) \mid \forall W' \sqsupseteq W. \forall(M_1, M_2) \in \mathcal{M}(W'). \\
 &\quad (v_1, v_2) \in \mathcal{B}(W') \wedge \\
 &\quad (\exists(u_1, u_2) \in \triangleright\mathcal{V}[\tau]\rho(W'). (v_1, M_1) \in \mathcal{L}_1.\text{ref}(u_1) \wedge (v_2, M_2) \in \mathcal{L}_2.\text{ref}(u_2)) \wedge \\
 &\quad (\forall(u_1, u_2) \in \triangleright\mathcal{V}[\tau]\rho(W'). (\mathcal{L}_1.\text{asgn}(M_1, v_1, u_1), \mathcal{L}_2.\text{asgn}(M_2, v_2, u_2)) \in \mathcal{M}(W')) \} \\
 \mathcal{V}[\mu\alpha.\tau]\rho &\stackrel{\text{def}}{=} \mu(F_{\alpha, \tau, \rho}) \\
 F_{\alpha, \tau, \rho} &\stackrel{\text{def}}{=} \lambda R. \{ (W, v_1, v_2) \in \text{oftype}(\mu\alpha.\tau, \rho) \mid \\
 &\quad \exists(u_1, u_2) \in \mathcal{V}[\tau]\rho[\alpha \mapsto (\rho_1(\mu\alpha.\tau), \rho_2(\mu\alpha.\tau), R)](W). \\
 &\quad (W, v_1, v_2) \in \square(\mathcal{L}_1.\text{roll}(u_1), \mathcal{L}_2.\text{roll}(u_2)) \} \\
 \mu(F)(W) &\stackrel{\text{def}}{=} F(\mu(F) \sqsupseteq W)(W) \\
 \mathcal{K}[\tau]\rho &\stackrel{\text{def}}{=} \{ (W, K_1, K_2) \in \text{World} \times \mathcal{L}_1.\text{Cont} \times \mathcal{L}_2.\text{Cont} \mid \forall W' \sqsupseteq_{\text{pub}} W. \\
 &\quad \forall(v_1, v_2) \in \mathcal{V}[\tau]\rho(W'). \forall(M_1, M_2) \in \mathcal{M}(W'). \\
 &\quad \forall C_1 \in \mathcal{L}_1.\text{plugv}(v_1, K_1, M_1). \forall C_2 \in \mathcal{L}_2.\text{plugv}(v_2, K_2, M_2). \\
 &\quad (C_1, C_2) \in \mathcal{O}(W') \} \\
 \mathcal{E}[\tau]\rho &\stackrel{\text{def}}{=} \{ (W, e_1, e_2) \in \text{World} \times \mathcal{L}_1.\text{Com} \times \mathcal{L}_2.\text{Com} \mid \\
 &\quad \forall(K_1, K_2) \in \mathcal{K}[\tau]\rho(W). \forall(M_1, M_2) \in \mathcal{M}(W). \\
 &\quad \forall C_1 \in \mathcal{L}_1.\text{plugc}(e_1, K_1, M_1). \forall C_2 \in \mathcal{L}_2.\text{plugc}(e_2, K_2, M_2). \\
 &\quad (C_1, C_2) \in \mathcal{O}(W) \}
 \end{aligned}$$

Thm (Monotonicity)
 $v_1 \approx_{\tau} v_2 : W$
 $\Rightarrow \forall W' \sqsupseteq W.$
 $v_1 \approx_{\tau} v_2 : W'$

\sqsupseteq	$\stackrel{\text{def}}{=} \{ (W', W) \mid \text{lev}(W) > 0 \wedge W' \sqsupseteq \triangleright W \}$
WVRel	$\stackrel{\text{def}}{=} \{ R \in \mathbb{P}(\text{World} \times \mathcal{L}_1.\text{Val} \times \mathcal{L}_2.\text{Val}) \}$
$R(W)$	$\stackrel{\text{def}}{=} \{ (v_1, v_2) \mid (W, v_1, v_2) \in R \}$
$\triangleright R$	$\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \mid \text{lev}(W) > 0 \implies (\triangleright W, v_1, v_2) \in R \}$
$\square R$	$\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \mid \forall W' \sqsupseteq W. (W', v_1, v_2) \in R \}$
$R \sqsupseteq_{\triangleright} W$	$\stackrel{\text{def}}{=} \{ (W', v_1, v_2) \mid W' \sqsupseteq W \wedge (W', v_1, v_2) \in R \}$
(R_1, R_2)	$\stackrel{\text{def}}{=} \{ (W, v_1, v_2) \mid \forall(M_1, M_2) \in \mathcal{M}(W). (v_1, M_1) \in R_1 \wedge (v_2, M_2) \in R_2 \}$ for $R_1 \in \mathbb{P}(\mathcal{L}_1.\text{Val} \times \mathcal{L}_1.\text{Mem}), R_2 \in \mathbb{P}(\mathcal{L}_2.\text{Val} \times \mathcal{L}_2.\text{Mem})$
TyValRel	$\stackrel{\text{def}}{=} \{ (\tau_1, \tau_2, R) \mid \tau_1, \tau_2 \in \text{CType} \wedge R \in \text{WVRel} \}$
ρ	$\in \text{TypeVar} \rightarrow \text{TyValRel}$
$\rho_1(\tau)$	$\stackrel{\text{def}}{=} \tau[\rho(\alpha).\tau_1/\alpha]$
$\rho_2(\tau)$	$\stackrel{\text{def}}{=} \tau[\rho(\alpha).\tau_2/\alpha]$
$\text{oftype}(\tau, \rho)$	$\stackrel{\text{def}}{=} \square(\mathcal{L}_1.\text{oftype}(\rho_1(\tau)), \mathcal{L}_2.\text{oftype}(\rho_2(\tau)))$

Logical Relation : details

$$\mathcal{V}[\tau' \rightarrow \tau]\rho \stackrel{\text{def}}{=} \{ (W, \mathbf{v}_1, v_2) \in \text{oftype}(\tau' \rightarrow \tau, \rho) \mid \forall W' \supseteq W. \forall (\mathbf{u}_1, u_2) \in \mathcal{V}[\tau']\rho(W') \\ \forall e_1 \in \mathcal{L}_1.\text{app}(\mathbf{v}_1, \mathbf{u}_1). \forall e_2 \in \mathcal{L}_2.\text{app}(v_2, u_2). (W', e_1, e_2) \in \mathcal{E}[\tau]\rho \}$$

$$\mathcal{V}[\text{ref } \tau]\rho \stackrel{\text{def}}{=} \{ (W, \mathbf{v}_1, v_2) \in \text{oftype}(\text{ref } \tau, \rho) \mid \forall W' \supseteq W. \forall (\mathbf{M}_1, M_2) \in \mathcal{M}(W'). \\ (\mathbf{v}_1, v_2) \in \mathcal{B}(W') \wedge \\ (\exists (\mathbf{u}_1, u_2) \in \triangleright \mathcal{V}[\tau]\rho(W'). (\mathbf{v}_1, \mathbf{M}_1) \in \mathcal{L}_1.\text{ref}(\mathbf{u}_1) \wedge (v_2, M_2) \in \mathcal{L}_2.\text{ref}(u_2)) \wedge \\ (\forall (\mathbf{u}_1, u_2) \in \triangleright \mathcal{V}[\tau]\rho(W'). (\mathcal{L}_1.\text{asgn}(\mathbf{M}_1, \mathbf{v}_1, \mathbf{u}_1), \mathcal{L}_2.\text{asgn}(M_2, v_2, u_2)) \in \mathcal{M}(W')) \}$$

supports both (iso-recursive types \rightsquigarrow High
equi-recursive types \rightsquigarrow Low

language-generic world model

World : LangSpec \times LangSpec \rightarrow WorldSpec

\hookrightarrow a functor implementing

Dreyer et al.'s possible worlds model

It can be built on arbitrary LangSpecs!

Low Spec

Loc	$\stackrel{\text{def}}{=} \{l \in \mathbb{N}\}$
Word	$\stackrel{\text{def}}{=} \{w \in \mathbb{N}\}$
$v \in \text{Val}$	$::= \underline{w} \mid \widehat{l}$
$lv \in \text{Lvalue}$	$::= [r] \mid \langle a \rangle_s \mid \langle r - o \rangle_s \mid \langle l : o \rangle_h \mid \langle r + o \rangle_h$
$rv \in \text{Rvalue}$	$::= lv \mid v$
Com	$\stackrel{\text{def}}{=} \{e = (\text{cpc}, \text{kpc}, \text{vloc}, \text{data})$ $\quad \in \text{Rvalue} \times \text{Rvalue} \times \text{Lvalue} \times \mathbb{P}(\text{Mem}) \}$
Cont	$\stackrel{\text{def}}{=} \{K = (\text{kpc}, \text{vloc}) \in \text{PAddr} \times \text{Lvalue}\}$
CodeFrag	$\stackrel{\text{def}}{=} \text{PAddr} \rightarrow_{\text{fin}} \text{Instruction}$
RegFiles	$\stackrel{\text{def}}{=} (\text{Register} \setminus \{\text{sp}\} \rightarrow \text{Val}) \uplus \{\text{undef}\}$
List X	$\stackrel{\text{def}}{=} \{(x_0, \dots, x_{n-1}) \mid n \in \mathbb{N} \wedge x_0, \dots, x_{n-1} \in X\}$
Stack	$\stackrel{\text{def}}{=} \text{List Val} \uplus \{\text{undef}\}$
Heap	$\stackrel{\text{def}}{=} \text{Loc} \rightarrow_{\text{fin}} \text{List Val}$
Table	$\stackrel{\text{def}}{=} (\text{Loc} \rightarrow \mathbb{N} \times \text{PAddr}) \uplus \{\text{undef}\}$
SysHeap	$\stackrel{\text{def}}{=} (\text{PAddr} \rightarrow \text{Word}) \uplus \{\text{undef}\}$
Mem	$\stackrel{\text{def}}{=} \{M = (\text{code}, \text{reg}, \text{stk}, \text{hp}, \text{tab}, \text{shp})$ $\quad \in \text{CodeFrag} \times \text{RegFiles} \times \text{Stack} \times \text{Heap} \times \text{Table} \times \text{SysHeap}\}$
Conf	$\stackrel{\text{def}}{=} \text{PConf}$
$\text{plugv}(v, K, M)$	$\stackrel{\text{def}}{=} \{(\Phi, \text{pc}) \in \text{Conf} \mid M \text{ repr } \Phi \wedge$ $\quad \text{pc} = K.\text{kpc} \wedge M(K.\text{vloc}) = v \}$
$\text{plugc}(e, K, M)$	$\stackrel{\text{def}}{=} \{(\Phi, \text{pc}) \in \text{Conf} \mid M \text{ repr } \Phi \wedge M \in e.\text{data} \wedge$ $\quad \text{pc} = M(e.\text{cpc}) \wedge M(e.\text{kpc}) = \underline{K.\text{kpc}} \wedge e.\text{vloc} = K.\text{vloc} \}$

$$\begin{aligned}
\text{oftype}(\tau) &\stackrel{\text{def}}{=} \{ (\mathbf{v}, \mathbf{M}) \in \text{Val} \times \text{Mem} \mid \\
&\quad \forall \tau_1, \tau_2. \tau = \tau_1 \rightarrow \tau_2 \implies \exists \mathbf{l}, w. \mathbf{v} = \widehat{\mathbf{l}} \wedge \mathbf{M}.\text{hp}(\mathbf{l})(0) = \underline{w} \wedge \\
&\quad \forall \alpha, \tau'. \tau = \forall \alpha. \tau' \implies \exists \mathbf{l}, w. \mathbf{v} = \widehat{\mathbf{l}} \wedge \mathbf{M}.\text{hp}(\mathbf{l})(0) = \underline{w} \} \\
\text{base}_b(x) &\stackrel{\text{def}}{=} \{ (\mathbf{v}, \mathbf{M}) \in \text{Val} \times \text{Mem} \mid \mathbf{v} \text{ is a representation of } x \} \\
\text{pair}(\mathbf{v}_1, \mathbf{v}_2) &\stackrel{\text{def}}{=} \{ (\mathbf{v}, \mathbf{M}) \in \text{Val} \times \text{Mem} \mid \exists \mathbf{l}. \mathbf{v} = \widehat{\mathbf{l}} \wedge \\
&\quad \mathbf{M}.\text{hp}(\mathbf{l})(0) = \mathbf{v}_1 \wedge \mathbf{M}.\text{hp}(\mathbf{l})(1) = \mathbf{v}_2 \} \\
\text{app}(\mathbf{v}_1, \mathbf{v}_2) &\stackrel{\text{def}}{=} \{ \mathbf{e} \in \text{Com} \mid \exists \mathbf{l}. \mathbf{v}_1 = \widehat{\mathbf{l}} \wedge \\
&\quad \mathbf{e}.\text{cpc} = \langle \mathbf{l} : 0 \rangle_{\text{h}} \wedge \mathbf{e}.\text{kpc} = [\text{wk}_0] \wedge \mathbf{e}.\text{vloc} = [\text{wk}_5] \wedge \\
&\quad \mathbf{e}.\text{data} = \{ \mathbf{M} \in \text{Mem} \mid \mathbf{M}.\text{reg}(\text{wk}_1) = \mathbf{v}_1 \wedge \mathbf{M}.\text{reg}(\text{wk}_2) = \mathbf{v}_2 \} \} \\
\text{appty}(\mathbf{v}, \tau) &\stackrel{\text{def}}{=} \{ \mathbf{e} \in \text{Com} \mid \exists \mathbf{l}. \mathbf{v} = \widehat{\mathbf{l}} \wedge \\
&\quad \mathbf{e}.\text{cpc} = \langle \mathbf{l} : 0 \rangle_{\text{h}} \wedge \mathbf{e}.\text{kpc} = [\text{wk}_0] \wedge \mathbf{e}.\text{vloc} = [\text{wk}_5] \wedge \\
&\quad \mathbf{e}.\text{data} = \{ \mathbf{M} \in \text{Mem} \mid \mathbf{M}.\text{reg}(\text{wk}_1) = \mathbf{v} \} \} \\
\text{pack}(\tau, \mathbf{v}) &\stackrel{\text{def}}{=} \{ (\mathbf{v}', \mathbf{M}) \in \text{Val} \times \text{Mem} \mid \mathbf{v}' = \mathbf{v} \} \\
\text{roll}(\mathbf{v}) &\stackrel{\text{def}}{=} \{ (\mathbf{v}', \mathbf{M}) \in \text{Val} \times \text{Mem} \mid \mathbf{v}' = \mathbf{v} \} \\
\text{ref}(\mathbf{v}) &\stackrel{\text{def}}{=} \{ (\mathbf{v}', \mathbf{M}) \in \text{Val} \times \text{Mem} \mid \exists \mathbf{l}. \mathbf{v}' = \widehat{\mathbf{l}} \wedge \mathbf{M}.\text{hp}(\mathbf{l})(0) = \mathbf{v} \} \\
\text{asgn}(\mathbf{M}, \mathbf{v}_1, \mathbf{v}_2) &\stackrel{\text{def}}{=} \begin{cases} \mathbf{M}[\mathbf{l} : 0 \mapsto \mathbf{v}_2]_{\text{hp}} & \text{if } \mathbf{v}_1 = \widehat{\mathbf{l}} \wedge |\mathbf{M}.\text{hp}(\mathbf{l})| > 0 \\ \text{undef} & \text{otherwise} \end{cases}
\end{aligned}$$

Specification of GC

$$\begin{aligned}
 \mathbf{v \text{ live in } M} &\stackrel{\text{def}}{=} \begin{cases} \top & \text{if } v = \underline{w} \\ \exists n, a. M.\text{tab}(l) = (n, a) \wedge n > 0 & \text{if } v = \widehat{l} \end{cases} \\
 \text{reach}_0(M) &\stackrel{\text{def}}{=} \{ l \mid \exists r \in \text{Register}. \widehat{l} = M.\text{reg}(r) \} \cup \\
 &\quad \{ l \mid \exists j < |M.\text{stk}|. \widehat{l} = M.\text{stk}(j) \} \\
 \text{reach}_{i+1}(M) &\stackrel{\text{def}}{=} \text{reach}_i(M) \cup \\
 &\quad \{ l \mid \exists l' \in \text{reach}_i(M). \exists j. \widehat{l} = M.\text{hp}(l')(j) \} \\
 \text{reach}(M) &\stackrel{\text{def}}{=} \bigcup_{i \in \mathbb{N}} \text{reach}_i(M) \\
 \text{GCSpec} &\stackrel{\text{def}}{=} \\
 \{ \mathcal{G} \in \text{PAddr} \rightarrow \{ &(\text{init}, \text{alloc}, \text{code}, I) \\
 &\in \text{PAddr} \times \text{PAddr} \times \text{List Instruction} \times \\
 &\quad \mathbb{P}(\text{Table} \times \text{SysHeap}) \} \mid \\
 \forall \text{gcbg}, \Phi, \text{pc}. & \\
 \Phi.\text{code} \supseteq [\text{gcbg} \mapsto \mathcal{G}(\text{gcbg}).\text{code}] \wedge \Phi.\text{reg}(\text{wk}_4) = \underline{\text{pc}} \implies & \\
 \exists M', \Phi'. & \\
 (\Phi, \mathcal{G}(\text{gcbg}).\text{init}) \xrightarrow{*} (\Phi', \text{pc}) \wedge & \\
 \Phi'.\text{code} = \Phi.\text{code} \wedge M'.\text{code} = [\text{gcbg} \mapsto \mathcal{G}(\text{gcbg}).\text{code}] \wedge & \\
 M' \text{ repr } \Phi' \wedge M' \in \mathcal{G}.GR(\text{gcbg}) \wedge M' \in \mathcal{G}.MR(\text{gcbg}) \wedge & \\
 \forall \text{gcbg}, M, \Phi, \text{pc}, n. & \\
 M \text{ repr } \Phi \wedge M \in \mathcal{G}.GR(\text{gcbg}) \wedge M \in \mathcal{G}.MR(\text{gcbg}) \wedge & \\
 M.\text{reg}(\text{wk}_4) = \underline{\text{pc}} \wedge M.\text{reg}(\text{wk}_5) = \underline{n} \implies & \\
 \exists \Phi', M', T, S, w, l, w_0, \dots, w_{n-1}. & \\
 (\Phi, \mathcal{G}(\text{gcbg}).\text{alloc}) \xrightarrow{*} (\Phi', \text{pc}) \wedge & \\
 M' \text{ repr } \Phi' \wedge M' \in \mathcal{G}.GR(\text{gcbg}) \wedge M' \in \mathcal{G}.MR(\text{gcbg}) \wedge & \\
 M' = M[[T, S]][\text{wk}_4 \mapsto \underline{w}]_{\text{reg}}[\text{wk}_5 \mapsto \widehat{l}]_{\text{reg}} \uplus & \\
 [l \mapsto (\underline{w}_0, \dots, \underline{w}_{n-1})]_{\text{hp}} \} & \\
 \mathcal{G}.GR(\text{gcbg}) &\stackrel{\text{def}}{=} \{ M \in \text{Mem} \mid \forall l \in \text{reach}(M). \widehat{l} \text{ live in } M \} \\
 \mathcal{G}.MR(\text{gcbg}) &\stackrel{\text{def}}{=} \{ M \in \text{Mem} \mid (M.\text{tab}, M.\text{shp}) \in \mathcal{G}(\text{gcbg}).I \wedge \\
 &\quad M.\text{code} \supseteq [\text{gcbg} \mapsto \mathcal{G}(\text{gcbg}).\text{code}] \}
 \end{aligned}$$

Mark-Sweep & Copying GC
satisfy



Spec of init

Spec of alloc

→ all reachable memories are live
→ private invariant of GC

Program Equivalence

$$\mathcal{H}.\text{Prog} \stackrel{\text{def}}{=} \{ e \mid \text{floc}(e) = \emptyset \}$$

$$\mathcal{L}.\text{Prog} \stackrel{\text{def}}{=} \{ p \in \text{PAddr} \times \text{PAddr} \rightarrow \text{List Instruction} \}$$

$$\mathcal{D}[\cdot] \stackrel{\text{def}}{=} \emptyset$$

$$\mathcal{D}[\Delta, \alpha] \stackrel{\text{def}}{=} \{ (\rho, \alpha \mapsto R) \mid \rho \in \mathcal{D}[\Delta] \wedge R \in \text{TyValRel} \}$$

$$\mathcal{G}[\cdot]\rho \stackrel{\text{def}}{=} \{ (W, \mathbf{v}, \emptyset) \mid W \in \text{World} \wedge \mathbf{v} \in \mathcal{L}.\text{Val} \}$$

$$\mathcal{G}[\Gamma, x : \tau]\rho \stackrel{\text{def}}{=} \{ (W, \mathbf{v}, (\gamma, x \mapsto v)) \mid \exists \mathbf{v}_1, \mathbf{v}_2. \\ \overline{(W, \mathbf{v}, \langle \rangle) \in \square(\mathcal{L}.\text{pair}(\mathbf{v}_1, \mathbf{v}_2), \mathcal{H}.\text{Val} \times \mathcal{H}.\text{Mem}) \wedge} \\ (W, \mathbf{v}_1, v) \in \mathcal{V}[\tau]\rho \wedge (W, \mathbf{v}_2, \gamma) \in \mathcal{G}[\Gamma]\rho} \}$$

$$W_k^\circ(\mathcal{G}, \text{gcbg}) \stackrel{\text{def}}{=} (k, [\iota^{\text{regstk}}, \iota^{\text{htyping}}, \iota^{\text{gc}}(\mathcal{G}, \text{gcbg})], GR^\circ(\mathcal{G}, \text{gcbg}))$$

$$\Delta; \Gamma \vdash p \approx e : \tau \stackrel{\text{def}}{=} \dots$$

$$\emptyset; \Delta; \Gamma \vdash e : \tau \wedge$$

$$\forall \mathcal{G}, \text{gcbg}, \text{bg}. \forall k, W \supseteq W_k^\circ(\mathcal{G}, \text{gcbg}). \forall (\mathbf{M}, M) \in \mathcal{M}(W).$$

$$\forall \mathbf{M}'. \mathbf{M}' = \mathbf{M} \uplus [\text{bg} \mapsto p(\mathcal{G}(\text{gcbg}).\text{alloc}, \text{bg})]_{\text{code}} \implies$$

$$\exists W' \supseteq W. \text{lev}(W') = \text{lev}(W) \wedge (\mathbf{M}', M) \in \mathcal{M}(W') \wedge$$

$$\forall W'' \supseteq W'. \forall \rho \in \mathcal{D}[\Delta]. \forall (\mathbf{v}, \gamma) \in \mathcal{G}[\Gamma]\rho(W'').$$

$$((\underline{\text{bg}}, [\text{wk}_0], [\text{wk}_5], \{ \mathbf{M} \mid \mathbf{M}.\text{reg}(\text{sv}_0) = \mathbf{v} \}), \gamma \rho e) \in \mathcal{E}[\tau]\rho(W'')$$

$$\text{where } \gamma \rho e ::= e[\rho(\alpha).\tau_2/\alpha][\gamma(x)/x].$$

Results

$$\Delta; \Gamma \vdash p \approx e : \tau$$

- ① Adequacy
- ② Compositionality
- ③ Compiler Correctness for a simple compiler

Summary

Summary

- Language-generic logical relation
- Adq & Compositional relation between Low & High
- use of Logical Memory for G.C.
- Compositional Compiler Correctness

Comments : Low-Low, High-High relations : no problem!
Generational GC : no problem!

Future work

- Multi-phase compiler
- Coq formalization
- Concurrency
- Self-modifying code applications :
dynamic linking & loading ; dynamic code generation
JIT compiler
- input, output
- other languages
Assem-C , C-ML

Thank you !!